

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention

Method and Apparatus for Using Connection Graphs with
Potential Diagonal Edges to Model Interconnect Topologies During
Placement

Application Number : 09/737245
Confirmation Number: 8353
First Named Applicant: Steven Teig, et al.
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Art Unit: 2825
Examiner: Thuan V Do



Search string: (4777606 or 5224057 or 5519836 or 5699265 or 6038385 or 6085032 or 6088519
or 6154874 or 6240541 or 6349403 or 6366279 or 6442745 or 6505331 or 6601227
or 6651233).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4777606	1988-10-11	Fournier			
	2	5224057	1993-06-29	Igarashi, et al.			
	3	5519836	1996-05-21	Gawlick, et al.			
	4	5699265	1997-12-16	Scepanovic, et al.			
	5	6038385	2000-03-14	Scepanovic, et al.			
	6	6085032	2000-07-04	Scepanovic, et al.			
	7	6088519	2000-07-11	Koford			
	8	6154874	2000-11-28	Scepanovic, et al.			
	9	6240541	2001-05-29	Yasuda, et al.			
	10	6349403	2002-02-19	Dutta, et al.			
	11	6366279	2002-02-04	Gorman	B1		
	12	6442745	2002-08-27	Arunachalam, et al.	B1		
	13	6505331	2003-01-07	Bracha, et al.	B1		
	14	6601227	2003-07-21	Trimberger	B1		
	15	6651233	2003-11-18	Teig, et al.			

Signature

Examiner Name	Date